1. **Name:** Arun Ravindran **Academic Rank:** Associate Professor

2. **Degrees:**

B.Eng	Birla Institute of Technology and Science, Pilani, India	1996
M.Eng	Birla Institute of Technology and Science, Pilani, India	1997
MS	Ohio State University	2000
PhD	Ohio State University	2003

3. **UNC Charlotte (number of years = 13)**
   - Associate Professor: 2009 – present
   - Assistant Professor: 2003 - 2009

4. **Other related experience:**
   - Engineer, IBM, 1997

5. **Certifications or professional registrations**: None

6. **Current membership in professional organizations**
   - ACM Member (2015- Present)

7. **Honors and awards:**
   - UNC Charlotte College of Engineering Undergraduate Award in Teaching Excellence, 2008.

8. **Institutional and professional service in the last five years:**
   - UNC Charlotte ECE, Faculty Hiring committee (Chair), 2015- 2016
   - UNC Charlotte ECE, Faculty Hiring committee (Member), 2014- 2015
   - National Science Foundation, SBIR Review Panel, Served 7 times, 2012- 2015
   - Computer Engineering, UG Academic Advisor, 2014-2016

9. **Principal publications of last five years:**

- **A. Ravindran,** and D. Mehta, "Data Structures for Big Data Stores", Accepted, CRC Computer and Information Science Series, 2016.
- D. Mehta, **A. Ravindran,** B. Joshi, S. Kamalasadan, "Graph Theory Based Online Optimal Power Control of Power Grid with Distributed Flexible AC Transmission Systems (D-FACTS) Devices, North American Power Symposium, October, 2015.
- G. Cao and **A. Ravindran**, "Energy Efficient Real-Time Computing through Cross-Layer Predictive Control", *9th International Workshop on Feedback Computing*, Philadelphia, June 2014.
- C. Zhang, and **A. Ravindran**, "A Statistical Machine Learning Based Modeling and Exploration Framework for Run-time Cross-Stack Energy Optimization", *IEEE International Symposium on Performance Analysis of Systems and Software (ISPASS)*, Austin, April 2013.
- K. Datta, A. Mukherjee, G. Cao, R. Tenneti, V. Lakshmi, **A. Ravindran**, and B. Joshi, "CASPER: Embedding Power Estimation and Hardware-Controlled Power Management in a Cycle-Accurate Micro-Architecture Simulation Platform for Many-Core Multi-Threading Heterogeneous Processors", *Journal of Low Power Electronics*. 2012, 2(1), 30-68
- C. Zhang, **A. Ravindran**, K. Datta, A. Mukherjee, and B. Joshi, "A Machine Learning Approach to Modeling Power and Performance of Chip Multiprocessors", *IEEE International Conference on Computer Design*, Amherst, October 2011.

10. Professional development activities in the last five years:

- UNC Charlotte Top 40 Academy Program, 2014- 2015.
- NSFCloud Workshop on Experimental Support for Cloud Computing, Dec 2014.