

1. **Name:** Ronald R. Sass

**Academic Rank:** Professor

2. **Degrees:**

|     |                           |      |
|-----|---------------------------|------|
| BS  | University of Toledo      | 1989 |
| MS  | Michigan State University | 1992 |
| PhD | Michigan State University | 1999 |

3. **UNC Charlotte (number of years = 10)**

- Professor: 2013 - present
- Associate Professor: 2006 - 2013

**University of Kansas (number of years = 2)**

- Assistant Research Professor - 2004-2006

**Clemson University (number of years = 3)**

- Assistant Professor - 1998-2004

4. **Other related experience:**

- Levine Faculty Fellow, 2013-2014
- Navy/SPAWAR Summer Faculty Fellow, 2013
- Navy/SPAWAR Summer Faculty Fellow, 2012

5. **Certifications or professional registrations:** None

6. **Current membership in professional organizations**

- IEEE (Senior Member), IEEE Computer Society (1985 – present)
- ACM (1985 – present)
- American Association for Advancement of Science, AAAS (2003 – present)

7. **Honors and awards:**

- Nominated for COE Teaching Excellence Award 2013.
- Finalist for 2014-2015 Harshini V. de Silva Graduate Mentor Award

8. **Institutional and professional service in the last five years:**

- UNC Charlotte Department Review Committee, (2013 - 2014)
- Chair, ECE Department Chair Search (2014-2015)
- Advisor, ECE Graduate Student Organization, student club (2013-2015)
- FCCM, Steering Committee and Program Committee (2003 – present)
- FPL, Program Committee (2004 – present)
- ReConFig, Program Committee and Track Chair (2005 – present)
- FPT, Program Committee (2008 – present)
- Charlotte Research Scholars (Research Experience for Undergraduates), 2013, 2015

## 9. Principal publications of last five years:

- Ron Sass and Andrew Schmidt, *Embedded Systems Design with Platform FPGAs*, Morgan-Kaufmann, an imprint of Elsevier, San Francisco, CA, USA}, 2010. (Textbook.)
- Shweta Jain and Ron Sass *The Case Study of Streaming Storage Format for Sparse Matrices*, IEEE International Conference on Reconfigurable Computing and {FPGA}'s (ReConFig 2012), 2012. **Best Paper Award.**
- Ashwin Mendon, Bin Huang, and Ron Sass, *A High-Performance, Open Source SATA2 Core*, International Conference on Field Programmable Logic and Applications (FPL 2012). **FPL Community Award.**
- Bin Huang, Andrew G. Schmidt, Ashwin A. Mendon, and Ron Sass, *Investigating Resilient High-Performance Reconfigurable Computing with Minimally-Invasive System Monitoring*, High-Performance Reconfigurable Computing Technology and Applications Workshop, SC 2010. **2010 OpenFPGA Award.**
- Andrew G. Schmidt, William V. Kritikos, Ron Sass, Erik K. Anderson, and Matthew French, *Merging Programming Models and On-Chip Networks to Meet the Programmable and Performance Needs of Multi-Core Systems on a Programmable Chip*, International Conference on ReConFigurable Computing and FPGAs (ReConFig 2010). **Best Paper Award.**
- PyDac: A Resilient Run-Time Framework for Divide-And-Conquer Applications on a Heterogeneous Many-Core Architecture, 6<sup>th</sup> Workshop on UnConventional High Performance Computing (UCHPC 2013). **Best Paper Award.**
- Scott Buscemi and Ron Sass, S. Buscemi and R. Sass, "Design of a Scalable Digital Wireless Channel Emulator for Networking Radios," Military Communications Conference (MILCOM), Charleston, 2011.

## 10. Professional development activities in the last five years:

- UNCC Summer Diversity Institute (2012)
- ABET Symposium, 2006 (trainee)